Shri Vaishnav Vidyapeeth Vishwavidyalaya
Master of Technology (Computer Science and Engineering)
Choice Based Credit System (CBCS)

SEMESTER - I

S. No.	COURSE CODE	COURSE NAME	Examination Scheme					Teaching Scheme/Week			CREDITS	TOTAL MARKS
			THEORY			PRACTICAL						
			End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	Th	T	P		
1	MTMA101	Advance Mathematics	60	20	20	0	0	3	1	0	4	100
2	MTCS102	Advance Data Structure & Algorithm	60	20	20	30	20	3	1	2	5	150
3	MTCS103	Advance Computer Network	60	20	20	30	20	3	1	2	5	150
4	MTCS110	Elective I	60	20	20	0	50	3	1	2	5	150
5	MTCS120	Elective II	60	20	20	30	20	3	1	2	5	150
6	MTCS106	Design Pattern Lab	0	0	0	0	50	0	0	2	1	50
7	MTCS107	MATLAB Lab	0	0	0	30	20	0	0	2	1	50
8	MTCS108	Comprehensive viva	0	0	0	0	50	0	0	8	4	50
			300	100	100	120	230	15	5	20	30	850

Elective-I			Elective-II	
MTCS111	Data Mining & Warehousing		MTCS121	Web Technology & E-commerce
MTCS112	Object Oriented Analysis & Design		MTCS122	Cloud Computing
MTCS113	Internet Of Things		MTCS123	Natural Language Processing
MTCS115	Big Data Analysis		MTCS124	Multimedia Computing

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.